

R E M A R K S

Careful review and examination of the subject application are noted and appreciated.

SUPPORT FOR CLAIM AMENDMENTS

Support for the amendments to the claims can be found in the drawings as originally filed, for example, FIGS. 1-4 and in the specification as originally filed, for example, on page 3, line 15 through page 6, line 10, on page 12, lines 4-18 and on page 16, lines 1-18. As such, no new matter has been added.

CLAIM REJECTIONS UNDER 35 U.S.C. §103

The rejection of claims 1-19, 22 and 25 under 35 U.S.C. §103(a) as being unpatentable over Borrás. (U.S. Patent No. 5,128,938) in view of Philips Semiconductors, "74HC/HCT5555 programmable delay timer with oscillator," September, 1993 (hereinafter Philips) has been obviated by appropriate amendment and should be withdrawn.

The rejection of claim 21 under 35 U.S.C. §103(a) as being unpatentable over Borrás in view of Philips, in further view of Yach et al. (U.S. Patent No. 5,454,114, hereinafter Yach) has been obviated by appropriate amendment and should be withdrawn.

The rejection of claims 20, 23 and 24 under 35 U.S.C. §103(a) as being unpatentable over Borrás in view of Philips, in

further view of Lee et al. (U.S. Patent No. 6,025,745, hereinafter Lee) has been obviated by appropriate amendment and should be withdrawn.

The presently claimed invention (claim 1) provides (a) a timer circuit configured to present any of a plurality of divided delay signals as a wake-up signal in response to (i) an input signal and (ii) an enable signal and (b) a microcontroller configured (i) to exit a suspend or sleep mode in response to the wake-up signal and (ii) to generate the input signal and the enable signal, where the input signal comprises a programmable delay value determined by the microcontroller during an awake mode in response to a predetermined delay value and the wake-up signal. Claims 14 and 15 include similar limitations.

Assuming the signal 236 of Borrás is similar to the presently claimed input signal (as suggested on page 2, lines 5-6 in section 2 of the Office Action and for which Applicant's representative does not necessarily agree), Borrás, Philips, Yach and Lee, alone or in combination, do not teach or suggest each and every element of the presently claimed invention. In particular, Borrás, Philips, Yach and Lee do not teach or suggest a microcontroller configured to generate the input signal **and** the enable signal, where the input signal comprises a programmable delay value **determined by the microcontroller** during an awake mode in response to a predetermined delay value and the wake-up signal,

as presently claimed. In particular, the microcontroller of Borrás does not determine a value for the signal 236. Instead, the microcontroller of Borrás **receives** the value to be used from the central 102 of Borrás (column 4, lines 66-68 of Borrás). As such, the presently claimed invention is fully patentable over the cited references and the rejections should be withdrawn.

Claims 2-13 and 16-25 depend, either directly or indirectly, from claims 1, 14 and 15 which are believed to be allowable. As such, the presently claimed invention is fully patentable over the cited references and the rejections should be withdrawn.

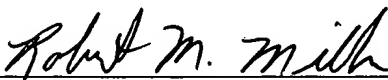
Accordingly, the present application is in condition for allowance. Early and favorable action by the Examiner is respectfully solicited.

The Examiner is respectfully invited to call the Applicant's representative should it be deemed beneficial to further advance prosecution of the application.

If any additional fees are due, please charge our office
Account No. 50-0541.

Respectfully submitted,

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